

# LA-2050/2025

Stand-Alone Logic Analyzer Series / Provide the best measurement solution/

## Introduction

The LA-20 Series helps minimize users' project risk by providing the most reliable, accurate data capture and a complete view of system behavior. These products are ideally suited for users on hardware/software debugging, parametric, mixed signal testing, and complex debugging. Moreover, their compact size and ability to connect with a PC for analysis and data logging makes them an ideal solution at remote sites.



- ### Standard Accessories
- Main unit .....x1
  - Data pod .....x4
  - Lead set.....x4
  - Testing probe (Testing hook) .....x36
  - AC power cord .....x1
  - CD.....x1  
(Driver and user manual are included)
  - USB cable.....x1

## Features

- Logic Analyzer:
  - External (synchronous) and internal(asynchronous) capture: Offer a more convenient environment for engineers.
  - Provide three sets of searching data functions and six cursor marks. The timing of each trigger point can be shown by the cursor mark.
  - Binary code and hexadecimal List mode (State) display.
  - Able to save measuring data and waveform results in stand-alone mode.
  - Offer I2C, SPI, UART and CAN signal decoding function on PC.
  - Provide various signal trigger and capture: Total of four kinds of trigger modes including Pattern/Edge/AND/OR.
  - Pre-trigger, post-trigger, 3 level trigger, and continued-trigger functions allow users to operate easily.
  - Bus analysis and glitch capture functions: 2M Bytes~4M Bytes long memory depth~each CH memory depth is up to 512Kbits~1Mbits.
  - The adjustable sample rate size can be set by users, which avoids long capturing time.
  - Provide "Trigger Counter" and "Pulse Wide Trigger" function.
  - High-speed Zoom In / Zoom Out techniques.
  - Smart software provides text file for saving the Binary Code of waveform results.
  - Compact, portable for engineers to perform debugging.
  - 5.6 inch TFT color LCD display.
  - USB 2.0 interface for PC link function, which can connect with PC for user to save, analyze, view and printout.

## Specification

Model	LA-2025	LA-2050
Timing Analysis	250MHz	500MHz
State Analysis	200MHz	200MHz
Bandwidth	200MHz	200MHz
Channels	32CH	32CH
RAM Size	2M Bytes	4M Bytes
Storage Depth per Channel	512K bits x 32CH	1M bits x 32CH
Maximum Input Voltage	±15V	±15V
Threshold Range	-4V~+4V	-4V~+4V
Data Skew (Channel to Channel)	4ns typical (±4ns Max)	2ns typical (±2ns Max)
Trigger Condition	Pattern / Edge / AND / OR	Pattern / Edge / AND / OR
Trigger Counter	1~255 times	1~255 times
Pulse Width Trigger	YES	YES
Glitch Capture	4ns	2ns
Display Measure	5.6 inch Colored LCD (320 x 234)	5.6 inch Colored LCD (320 x 234)
Communication	USB 2.0	USB 2.0
Power Source	110V AC~240V AC	110V AC~240V AC
Frequency Range	50~60Hz	50~60Hz
Power Consumption	18W (20W Max)	18W (20W Max)
Operating Temperature	0°C~45°C	0°C~45°C
Dimension	310 x 150 x 90mm	310 x 150 x 90mm
Weight	3.8 Kg	3.8 Kg

## PC System Requirement

Operating System	Win 8/ 7/ Vista/ XP
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### ■ Data Logger

- Provide friendly software, no matter what program language, just key in the timing,trigger condition and logic level etc, then start the data acquisition.
- Use binary for logging the code.